IN THE DRAWINGS:

Please replace Figures 1-6 of the drawings as originally filed with the attached replacement sheets. Additional sheets showing the changes made in red are also attached. No new matter has been added. Accordingly, it is respectfully requested that the enclosed drawings be approved and entered in the subject application.

REMARKS

Reconsideration of the above identified application in view of the preceding amendments and following remarks is respectfully requested. Claims 1-14 are pending in this application. By this Amendment, Applicants have and amended Claims 1 and 4. New Claims 9-14 have been added by this amendment. The claim amendments were made to more precisely define the invention in accordance with 35 U.S.C. 112, paragraph 2. These amendments have not been necessitated by the need to distinguish the present invention from any prior art. It is respectfully submitted that no new matter has been introduced by these amendments, as support therefor is found throughout the specification and drawings.

In the Office Action, the Examiner objected to the drawings. In response thereto, Figs. 1-6 have been amended to correct artists mistakes. Accordingly, the drawings are submitted herewith and withdrawal of the rejection is respectfully requested.

The specification was objected to. In this regard, the specification is amended hereby and withdrawal of the rejection is respectfully requested.

In the Office Action, Claims 1-3 were rejected under 35 U.S.C. §102(b) over U.S. Patent No. 6,172,574 to Sirito-Olivier. The Examiner's grounds for rejection are herewith traversed, and reconsideration is respectfully requested.

Sirito-Olivier discloses a resonance circuit 10 (a Colpitts-type oscillator). A quartz crystal 1 has two ends connected to terminals E and M. Capacitors Cbe, Ce are mounted between terminals E and M to form a series connection and allow a user to remove one altogether and/or modify the central oscillation frequency. In an oscillator

amplifier formed with transistors T4 and T5, the collectors of transistors T4 and T5 are connected, independently and via a series connection of resistors R4, R5, and R6, R7, to a positive supply Vcc. Thus, resistors R6, R7 are pull-up resistors. No pull-down resistors are provided therein. The midpoint 15 of the series association of resistors R6 and R7 is connected to the collector of transistor T7. Capacitor C3 is connected in parallel to resistor Re3. Capacitor C3 is not directly connected to the midpoint 15. Thus, the resonance circuit 10 is not directly or functionally connected to the oscillator amplifier (T4 and T5). Further, the resonance circuit 10 is not directly or functionally connected to the capacitor C3 or the resistor Re3.

In contrast, Claim 1 recites an oscillator circuit comprising a resonance circuit formed of a resonator as an inductor component and dividing capacitors, each dividing capacitors having a first end connected to the resonator and a second end connected to ground, an oscillation amplifier driven by a power voltage connected to said resonance circuit, and a pull-down resistor provided between an output terminal of said oscillation amplifier and ground, wherein said pull-down resistor is serially-connected dividing resistors, the serially-connected dividing resistors being a first pull-down resistor connected circuit side and a second pull-down resistor connected ground side, and also a bias capacitor is directly connected between a connection point between said dividing resistors and ground. Consequently, the dividing capacitors are not connected in series, rather the dividing capacitors are both necessary. Further, the dividing resistors act as pull-down resistors. Still further, the bias capacitor allows for greatly reducing the AC resistance and the high-frequency current flowing in the resonator 3 that is parallel to the pull-down resistors can be controlled, which prevents frequency variations. Sirito-

Olivier does not disclose or suggest such a structural configuration or provide for such advantages. As a result, Sirito-Olivier cannot achieve the remarkable features of the claimed invention and as set forth on pages 5 and 6 of the subject application. Accordingly, Claim 1 and each of the claims depending therefrom distinguish the subject invention from Sirito-Olivier and withdrawal of the rejection is respectfully requested.

In the Office Action, Claims 4-6 were rejected under 35 U.S.C. §102(b) over U.S. Patent No. 5,627,498 to Meyer. The Examiner's grounds for rejection are herewith traversed, and reconsideration is respectfully requested.

Meyer discloses a multiple frequency oscillator having first and second mirror circuits. Each mirror circuit has a pair of differential inverting transistors Q2, Q4 having their emitters connected to a node N2 through the collector-emitter path of a switching transistor Q6. A resonant feedback network FB2 is utilized and includes two varactor diodes DV2, DV4 in series. A crystal Y2 is connected in parallel with the varactor diodes DV2, DV4.

In contrast, Claim 4 recites a frequency-switching oscillator including a two input, two output type of oscillation amplifier having signals of mutually opposite phase is connected to a resonance circuit formed of a resonator and dividing capacitors, the dividing capacitors are connected to each end, respectively, of the resonator as well as each dividing capacitor being connected to ground; a first resonance circuit provided with a first electronic switch is connected between a pair of input-output terminals for signals of mutually opposite phase; and a second resonance circuit provided with a second electronic switch is connected between another pair of input-output terminals; wherein the resonance frequencies of said first and second resonance circuits are different and also

said first and second electronic switches are selectively switched to select one of said resonance circuits and further comprising a bias resistor connected to each input of the oscillation amplifier. Meyer does not disclose or suggest such a structural configuration of dividing capacitors. Accordingly, Claim 4 and each of the claims depending therefrom distinguish the subject invention from Meyer and withdrawal of the rejection is respectfully requested.

In the Office Action, Claims 7 and 8 were rejected under 35 U.S.C. § 103 (a) over Meyer. The Examiner's grounds for rejection are herewith traversed, and reconsideration is respectfully requested.

As noted above, there is nothing in Meyer that discloses or suggests, in whole or in part, the device defined by Claim 4 of the subject application. In particular, there is nothing in Meyer which discloses or suggests, *inter alia*, dividing capacitors are connected to each end, respectively, of the resonator as well as each dividing capacitor being connected to ground. Therefore, Claims 7 and 8, by virtue of their dependency upon Claim 4, are not rendered obvious by Meyer, and withdrawal of the rejection under 35 U.S.C. §103(a) is respectfully requested.

Any additional fees or overpayments due as a result of filing the present paper may be applied to Deposit Account No. 04-1105. It is respectfully submitted that all of the claims now remaining in this application, namely Claims 1-14, are in condition for allowance, and such action is earnestly solicited.

If after reviewing this amendment, the Examiner believes that a telephone interview would facilitate the resolution of any remaining matters the undersigned attorney may be contacted at the number set forth herein below.

Respectfully submitted,

Date: November 14, 2005

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FIG. 1

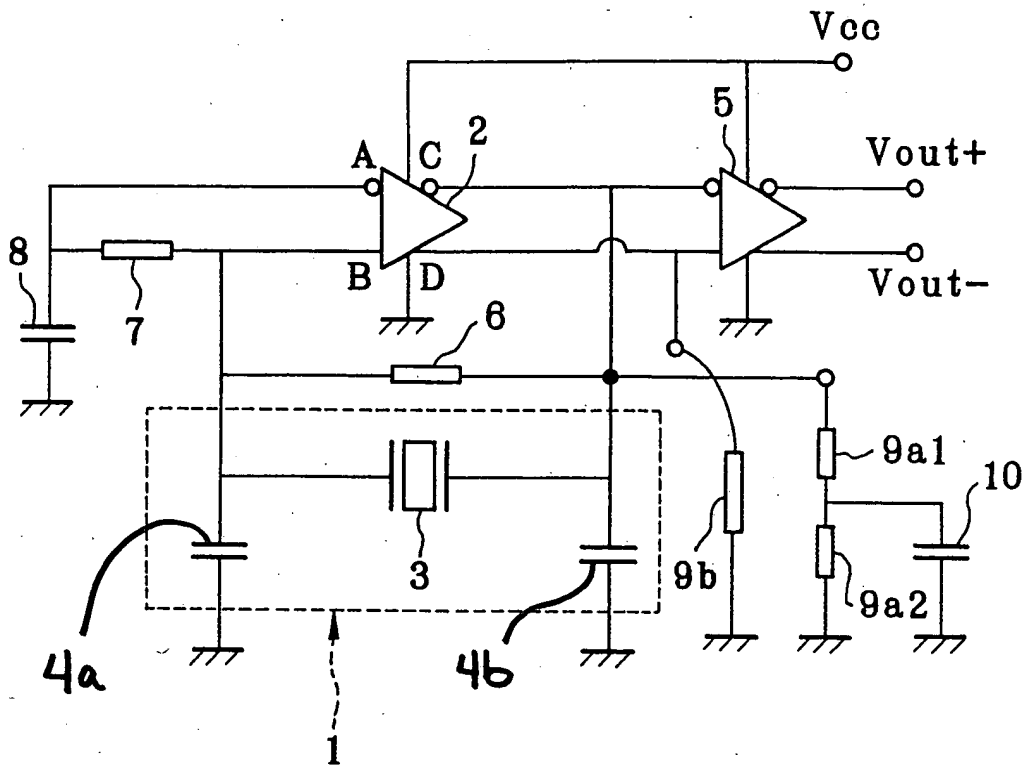

Vcc
5
2
A
C
Vout+
8
B
D
Vout-
7
6
9a1
10
3
9b
9a2
4a
4b
1

FIG. 2

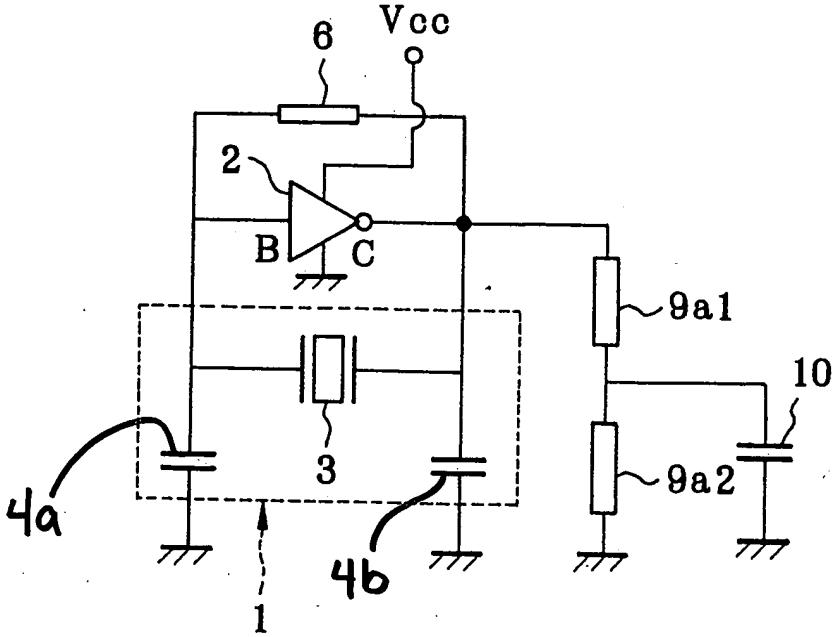

6
Vcc
2
B
C
9a1
10
3
9a2
4a
4b
1

FIG. 3

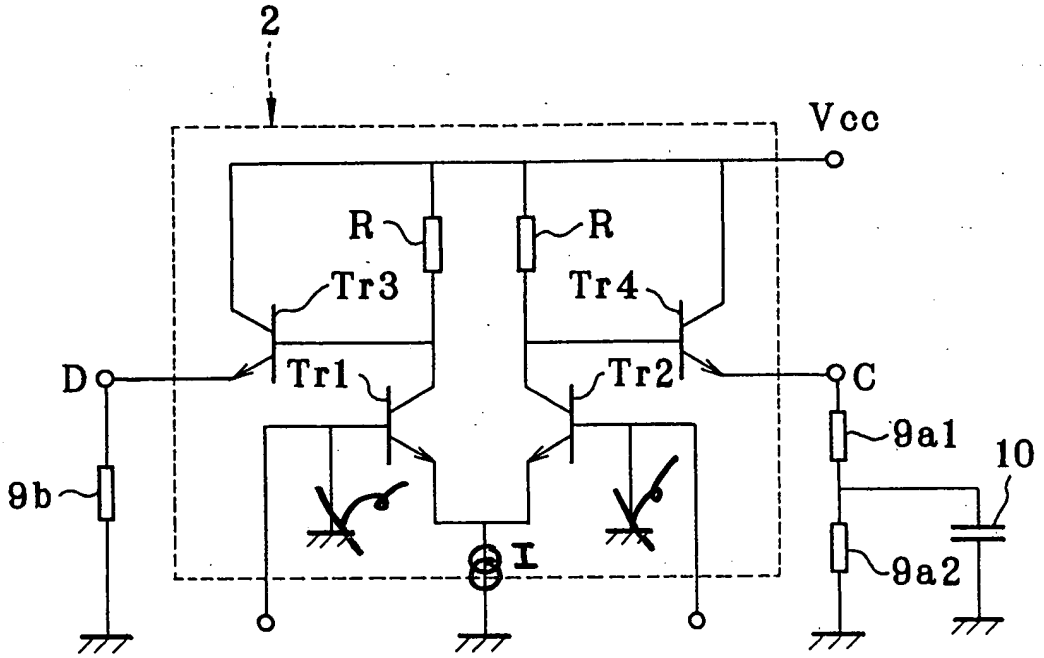

2
Vcc
R
R
Tr3
Tr4
D
Tr1
Tr2
C
9a1
10
9b
I
9a2

FIG. 4

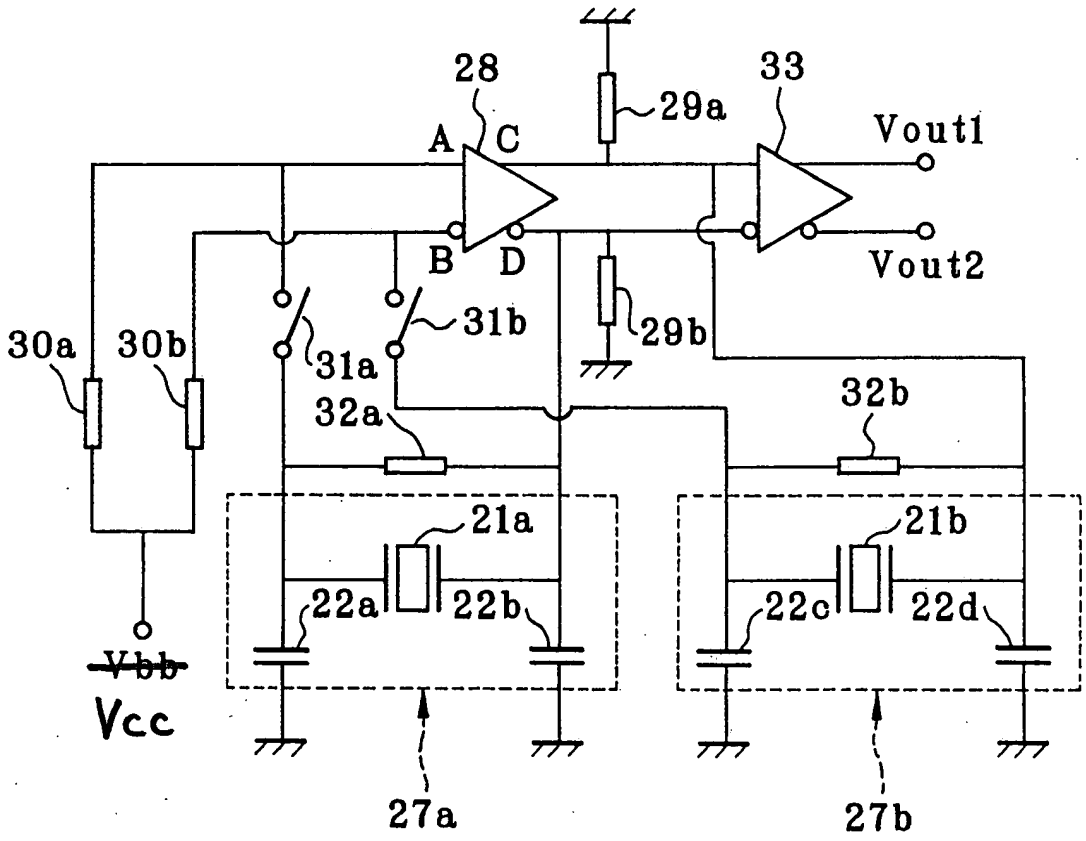

28
33
29a
A
C
Vout1
B
D
Vout2
30a
30b
31a
31b
29b
32a
32b
21a
21b
22a
22b
22c
22d
Vbb
Vcc
27a
27b

FIG. 5

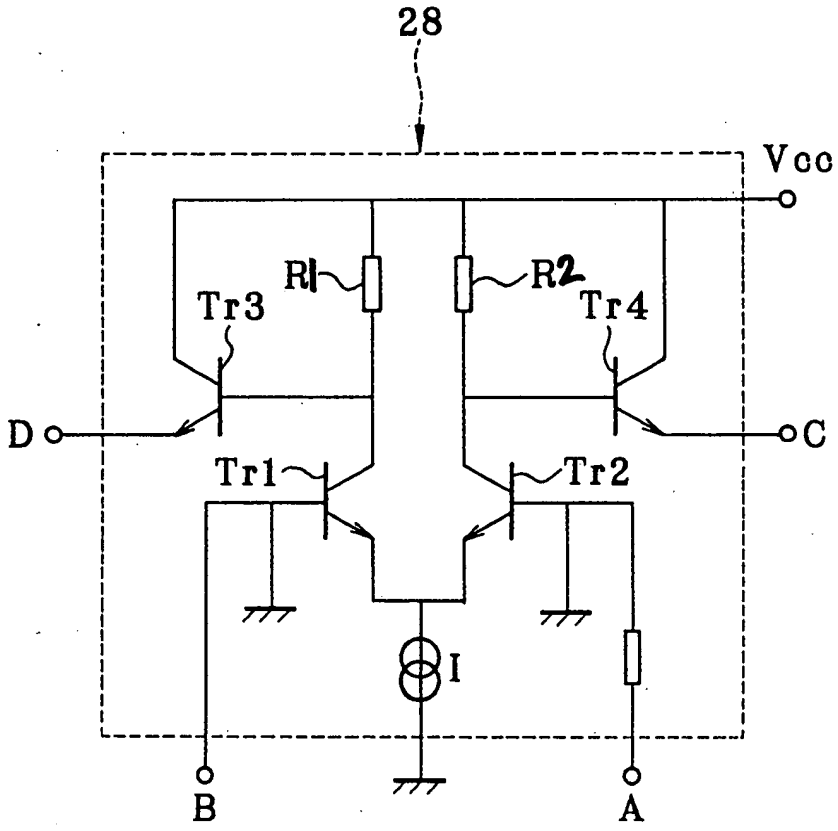
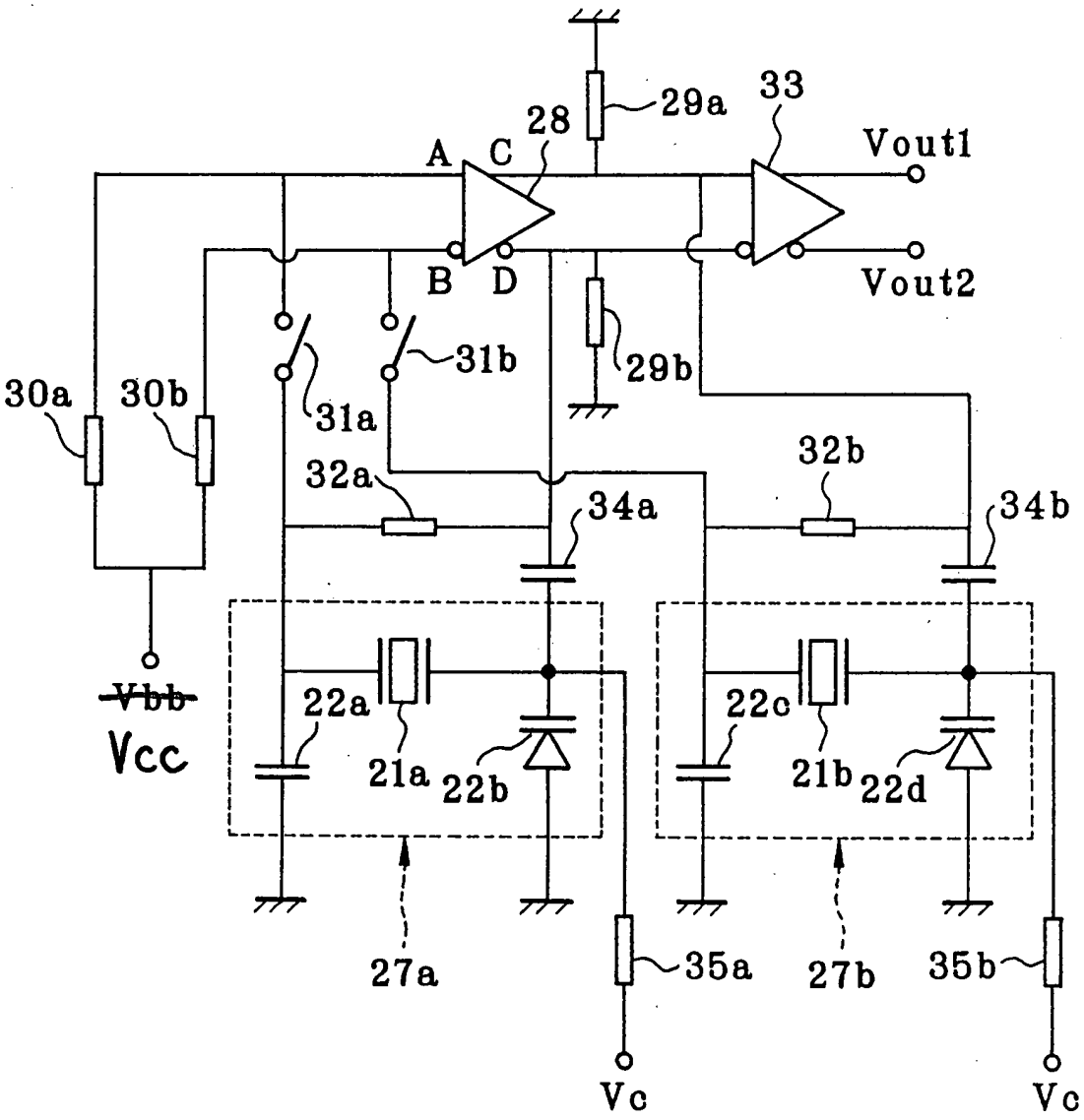


FIG. 6


29a
33
28
A
C
Vout1
B
D
Vout2
31b
29b
30a
30b
31a
32a
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34a
34b
Vbb
Vcc
22a
22c
21a
22b
21b
22d
27a
35a
27b
35b
Vc
Vc